L	Hits	Search Text	DB	Time stamp
Number -	164	(710/317).CCLS.	USPAT;	2003/12/22
_	18	((710/317).CCLS.) and analog and digital	US-PGPUB USPAT;	11:54 2003/12/19
-	37232	microcontroller	US-PGPUB USPAT;	14:17 2003/12/19
			US-PGPUB; EPO; JPO; DERWENT; IBM TDB	13:51
_	14360	microcontroller and analog and digital	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/12/19 13:51
_	2326	(microcontroller and analog and digital) and (wirebond or "wire bond" or pad)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/12/19 13:52
-	1714	((microcontroller and analog and digital) and (wirebond or "wire bond" or pad)) and processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/12/19 13:52
-	1459	digital) and (wirebond or "wire bond" or pad)) and processor) and (crossbar or switch\$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/19 13:55
_	1458	(((microcontroller and analog and digital) and (wirebond or "wire bond" or pad)) and processor) and (crossbar or switch\$5)	USPAT; US-PGPUB	2003/12/19 13:56
	37232	microcontroller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/19 14:11
-	316838	analog same digital	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/12/19 14:11
-	13231	microcontroller and (analog same digital)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/12/19 14:12
_	0	((710/317).CCLS.) and wirebond	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/12/19 14:12
-	8	(microcontroller and (analog same digital)) and wirebond	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/12/19 14:12
-	62	digital)) and (wirebond or "wire bond")	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/19 14:13
-	55	((microcontroller and (analog same digital)) and (wirebond or "wire bond")) and (processor or microprocessor or cpu)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/12/19 14:14 .

_	48	(((microcontroller and (analog same	USPAT;	2003/12/19
		digital)) and (wirebond or "wire bond"))	US-PGPUB;	14:16
		and (processor or microprocessor or cpu))	EPO; JPO;	
:		and (switch\$4 or crossbar or "cross bar")	DERWENT;	
		·	IBM_TDB	
-	0		USPAT;	2003/12/19
		digital)) and (wirebond or "wire bond"))	US-PGPUB;	14:16
		and (processor or microprocessor or cpu))	EPO; JPO;	
		and (switch\$4 or crossbar or "cross	DERWENT;	
		bar")) not microcontroller	IBM_TDB	1
_	316838	analog same digital	USPAT;	2003/12/19
			US-PGPUB;	14:17
•			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	803	, ,	USPAT;	2003/12/19
		"wire bond")	US-PGPUB;	14:17
		·	EPO; JPO;	
			DERWENT;	
			IBM_TDB	}
_	493		USPAT;	2003/12/19
		"wire bond")) and (switch\$4 or crossbar	US-PGPUB;	14:18
		or "cross bar")	EPO; JPO;	
			DERWENT;	
		· ·	IBM_TDB] .
-	301		USPAT;	2003/12/19
		"wire bond")) and (switch\$4 or crossbar	US-PGPUB;	17:05
		or "cross bar")) and (processor or	EPO; JPO;	
	1	microprocessor or cpu)	DERWENT;	
	1		IBM_TDB	
-	2	(("6144327") or ("5202687")).PN.	USPAT;	2003/12/19
			US-PGPUB	17:06
- ,	1	("6192431").PN.	USPAT;	2003/12/22
			US-PGPUB	13:19
-	0	6192431.URPN.	USPAT	2003/12/22
				13:15
-	0	6192431.URPN.	USPAT	2003/12/22
	_			13:15
_	6		USPAT	2003/12/22
	1.40	"5748982" "5822610" "5835965").PN.	l	13:15
-	1649	(257/666).CCLS.	USPAT;	2003/12/22
	1005	1.057.4555	US-PGPUB	13:19
_	1335		USPAT;	2003/12/22
		bond" or pad)	US-PGPUB	14:01
-	87	, , , , , , , , , , , , , , , , , , , ,	USPAT;	2003/12/22
		bond" or pad)) and (processor or cpu)	US-PGPUB	13:21
-	81	• • • • • • • • • • • • • • • • • • •	USPAT;	2003/12/22
		"wire bond" or pad)) and (processor or	US-PGPUB	13:23
		cpu)) and (switch or select\$6 or		
_	64	configura\$5)	IICDATE.	2002/12/22
-	64		USPAT; US-PGPUB	2003/12/22
		"wire bond" or pad)) and (processor or	02-16108	13:23
		cpu)) and (switch or select\$6 or configurable or reconfigurable or		
		re-configurable)		
_	429		USPAT;	2003/12/22
	429	((257/666).CCLS.) and (wirebond of wire bond")	US-PGPUB	14:03
l _	43	(((257/666).CCLS.) and (wirebond or "wire	USPAT;	2003/12/22
	43	(((257/666).CCES.) and (wirebond of wirebond of wirebond")) and (processor or cpu)	US-PGPUB	14:02
_	482	((257/666).CCLS.) and ((wirebond or "wire	USPAT;	2003/12/22
	1 402	((257/666).CCLS.) and ((wirebond of wire bond") next10 (processor or cpu))	US-PGPUB	14:03
_	422718	("wire bond" or wirebond) next10	USPAT;	2003/12/22
	122/10	(processor or cpu)	US-PGPUB	14:04
_	482	(("wire bond" or wirebond) next10	USPAT;	2003/12/22
	402	(processor or cpu)) and ((257/666).CCLS.)	US-PGPUB	14:04
l _	17	("wirebond pad" or "wire bond pad")	USPAT;	2003/12/22
	1	near10 (processor or cpu)	US-PGPUB	14:07
l -	48		USPAT;	2003/12/22
1		(processor or cpu)	US-PGPUB	14:08
	8	((wirebond or "wire bond") near10	USPAT;	2003/12/22
1		(processor or cpu)) same (switch or	US-PGPUB	14:10
		configura \$6 or select \$6)		

				·
-	1	("5563529").PN.	USPAT;	2003/12/22
	_	(#6445242#\ px	US-PGPUB	14:12
-	1	("6445242").PN.	USPAT; US-PGPUB	14:14
1_	1133	(257/676).CCLS.	USPAT;	2003/12/22
	1133	(2377 676).CCLS.	US-PGPUB	14:14
_	207	"wirebond pad"	USPAT;	2003/12/22
			US-PGPUB	14:15
_	1183	"wire bond pad"	USPAT;	2003/12/22
			US-PGPUB	14:15
-	1351	"wirebond pad" or "wire bond pad"	USPAT;	2003/12/22
			US-PGPUB	14:16
-	188	("wirebond pad" or "wire bond pad") and	USPAT;	2003/12/22
	2443436	processor switch\$6 or select\$6 or reconfigurable or	US-PGPUB USPAT;	14:17 2003/12/22
-	2443436	re-configurable or configurable	US-PGPUB	14:18
_	0	(("wirebond pad" or "wire bond pad")	USPAT;	2003/12/22
		near10 (processor or cpu)) near10	US-PGPUB	14:20
		(switch\$5 or select\$5 or configurable or		·
		reconfigurable or re-configurable)	,	
-	0	(("wirebond pad" or "wire bond pad")	USPAT;	2003/12/22
		near10 (processor or cpu)) same (switch\$5	US-PGPUB	14:20
1		or select\$5 or configurable or		
1_	1	reconfigurable or re-configurable) (("wirebond pad" or "wire bond pad")	HCDAT.	2003/12/22
1		near10 (processor or cpu)) and (switch\$5	USPAT; US-PGPUB	14:20
		or select\$5 or configurable or	05 13500	
		reconfigurable or re-configurable)		
-	11	(("wirebond pad" or "wire bond pad")	USPAT;	2003/12/22
		near10 (processor or cpu)) and (switch\$5	US-PGPUB	15:59
		or select\$5 or configurable or		·
	25.4	reconfigurable or re-configurable)	110000	0000/10/00
-	954	(326/38).CCLS.	USPAT;	2003/12/22 16:00
	2	((326/38).CCLS.) and ("wirebond pad" or	US-PGPUB USPAT;	2003/12/22
		"wire bond pad")	US-PGPUB	16:02
-	1058	(326/41).CCLS.	USPAT;	2003/12/22
		, , , , , , , , , , , , , , , , , , , ,	US-PGPUB	16:02
-	1	((326/41).CCLS.) and ("wirebond pad" or	USPAT;	2003/12/22
		"wire bond pad")	US-PGPUB	16:03
-	319	(257/e23.011).CCLS.	USPAT;	2003/12/22
		//257/022 011) GGTG \ and /!!	US-PGPUB	16:04
1-	3	((257/e23.011).CCLS.) and ("wirebond pad" or "wire bond pad")	USPAT; US-PGPUB	2003/12/22 16:06
_	65	(257/e23.032).CCLS.	USPAT;	2003/12/22
		,	US-PGPUB	16:07
	0	((257/e23.032).CCLS.) and ("wirebond pad"	USPAT;	2003/12/22
		or "wire bond pad")	US-PGPUB	16:07
-	534	(257/e23.079).CCLS.	USPAT;	2003/12/22
1		//057/ 02 070) 5555	US-PGPUB	16:07
-	11	((257/e23.079).CCLS.) and ("wirebond pad"	USPAT;	2003/12/22
	401	or "wire bond pad") (710/316).CCLS.	US-PGPUB USPAT;	16:11 2003/12/22
	1 401	(,10,510).0010.	US-PGPUB	16:12
_	0	((710/316).CCLS.) and ("wirebond pad" or	USPAT;	2003/12/22
		"wire bond pad")	US-PGPUB	16:12
-	164	(710/317).ccls.	USPAT;	2003/12/22
		·	US-PGPUB	17:36
-	0	((710/317).CCLS.) and ("wirebond pad" or	USPAT;	2003/12/22
	9.2	"wire bond pad")	US-PGPUB	16:26
-	82	((326/38).CCLS.) and ((326/41).CCLS.) and (pad and processor or microprocessor)	USPAT; US-PGPUB	2003/12/22 16:27
_	42	((326/38).CCLS.) and ((326/41).CCLS.) and	USPAT;	2003/12/22
	32	(pad and (processor or microprocessor))	US-PGPUB	16:27
-	35	(((326/38).CCLS.) and ((326/41).CCLS.)	USPAT;	2003/12/22
		and (pad and (processor or	US-PGPUB	16:28
	İ	microprocessor))) and (configurable or		
		reconfigurable or re-configurable)		

	7.	1 / / / 20 C / 20 \	TIGDAM.	1 2002 /12 /22
_	31	((((326/38).CCLS.) and ((326/41).CCLS.) and (pad and (processor or	USPAT; US-PGPUB	2003/12/22 16:31
		microprocessor))) and (configurable or	US FGFUD	10.31
	1	reconfigurable or re-configurable)) and		
		switch\$6		1
-	382	(configurable or reconfigurable or	USPAT;	2003/12/22
		re-configurable) near6 pin	US-PGPUB	16:50
-	70	((configurable or reconfigurable or re-configurable) near6 pin) and pad and	USPAT; US-PGPUB	2003/12/22 16:50
		(processor or microprocessor) and (switch	US-PGPUB	10:30
		or connect\$5 or select\$4)		
_	234	(configurable or reconfigurable or	USPAT;	2003/12/22
		re-configurable) near3 pin	US-PGPUB	16:50
-	34	((configurable or reconfigurable or	USPAT;	2003/12/22
		re-configurable) near3 pin) and pad and	US-PGPUB	17:33
		(processor or microprocessor) and (switch or connect\$5 or select\$4)	,	
_	2289	(257/666,676).CCLS.	USPAT;	2003/12/22
		(2017,000,0107,0025)	US-PGPUB	17:36
_	0	((257/666,676).CCLS.) and	USPAT;	2003/12/22
1		((326/38).CCLS.) and ((326/41).CCLS.) and	US-PGPUB	17:37
		((257/e23.011).CCLS.) and		
		((257/e23.032).CCLS.) and ((257/e23.079).CCLS.) and	,	
		((710/316).CCLS.) and ((710/317).CCLS.)		
		and microcontroller		
_	142	(((257/666,676).CCLS.) or	USPAT;	2003/12/22
		((326/38).CCLS.) or ((326/41).CCLS.) or	US-PGPUB	17:38
·		((257/e23.011).CCLS.) or		
		((257/e23.032).CCLS.) or		
		((257/e23.079).CCLS.) or ((710/316).CCLS.) or ((710/317).CCLS.))		
		and microcontroller		. [
_	71	(((257/666,676).CCLS.) or	USPAT;	2003/12/22
		((326/38).CCLS.) or ((326/41).CCLS.) or	US-PGPUB	17:39
		((257/e23.011).CCLS.) or		
.]		((257/e23.032).CCLS.) or		
		((257/e23.079).CCLS.) or ((710/316).CCLS.) or ((710/317).CCLS.))		
		and microcontroller and pad		
-	57	(((257/666,676).CCLS.) or	USPAT;	2003/12/22
		((326/38).CCLS.) or ((326/41).CCLS.) or	US-PGPUB	18:09
		((257/e23.011).CCLS.) or		
		((257/e23.032).CCLS.) or ((257/e23.079).CCLS.) or		
		((710/316).CCLS.) or ((710/317).CCLS.))		
		and microcontroller and pad and		
		(processor or microprocessor)		
-	0	6509758.URPN.	USPAT	2003/12/22
	10	/!! / 6 / 2 5 6 1 !! !! / 9 0 0 2 0 4 !! !! / 9 0 0 2 0 6 !!	HCDAM	17:50
-	12	("4642561" "4800294" "4963768" "5107146" "5107230" "5289116"	USPAT	2003/12/22 17:51
		"5473758" "5511182" "5563526"	,	17.51
		"5686844" "6057705" "6246258").PN.		
-	14	((((257/666,676).CCLS.) or	USPAT;	2003/12/22
		((326/38).CCLS.) or ((326/41).CCLS.) or	US-PGPUB	18:09
		((257/e23.011).CCLS.) or		
	!	((257/e23.032).CCLS.) or ((257/e23.079).CCLS.) or		
		((710/316).CCLS.) or ((710/317).CCLS.))		
		and microcontroller and pad) not		
	·	((((257/666,676).CCLS.) or		
		((326/38).CCLS.) or ((326/41).CCLS.) or		
		((257/e23.011).CCLS.) or ((257/e23.032).CCLS.) or		
		((257/e23.032).CCLS.) or ((257/e23.079).CCLS.) or		
,	İ	((710/316).CCLS.) or ((710/317).CCLS.))		
		and microcontroller and pad and		
	_	(processor or microprocessor))		
-	0	6509758.URPN.	USPAT	2003/12/24
L			L	13:48

	10	(44,6425,614, 1, 44,002,044, 1, 44,0627,694, 1	USPAT	2002/12/24
_	12	("4642561" "4800294" "4963768"	USPAT	2003/12/24
		"5107146" "5107230" "5289116"		13:48
		"5473758" "5511182" "5563526"		
		"5686844" "6057705" "6246258").PN.		
-	12	("4642561" "4800294" "4963768"	USPAT	2003/12/24
		"5107230" "5289116" "5473758"	•	13:49
		"5511182" "5563526" "5686844"		
÷		"5724009" "6057705" "6246258").PN.		
-	23	("4472647" "4698526" "4877978"	USPAT	2003/12/24
		"4896060" "4902917" "4930112"		13:50
		"4978905" "5084635" "5144167"		
		"5153450" "5157282" "5161124"		
		"5162672" "5300832" "5309044"		
		"5345112" "5353250" "5359240"		
		"5402018" "5406139" "5600267"		
•		"5732027" "6047352").PN.		
_	12	("4642561" "4800294" "4963768"	USPAT	2003/12/30
		"5107146" "5107230" "5289116"		13:41
		"5473758" "5511182" "5563526"		1 20.12
		"5686844" "6057705" "6246258").PN.		
_	0	6509758.URPN.	USPAT	2003/12/30
		0303730.0REN.	OSIAI	14:17
_	12	("4642561" "4800294" "4963768"	USPAT	2003/12/30
_	12	"5107230" "5289116" "5473758"	USPAI	14:17
		3107230	1	14.1/
	ر ا	"5724009" "6057705" "6246258").PN.	HODAM	2002/12/20
_	3	("5757207" "5768598" "5883526").PN.	USPAT	2003/12/30
		C100041 11PPV		17:28
-	0	6188241.URPN.	USPAT	2003/12/30
				17:29
-	6	("5701517" "5715197" "5737764"	USPAT	2003/12/30
		"5748982" "5822610" "5835965").PN.		17:29
_	27	"analog circuit" and "digital circuit"	USPAT;	2003/12/30
		and switch and pad and processor and	US-PGPUB	17:52
		(microcontroller or "micro controller" or		
		micro-controller)		_
-	0	20020108006.URPN.	USPAT	2003/12/30
				17:50
_	0	"analog circuit" and "digital circuit"	EPO; JPO;	2003/12/30
		and switch and pad and processor and	DERWENT;	17:52
		(microcontroller or "micro controller" or	IBM_TDB	
		micro-controller)	_	
_	36	"analog circuit" and "digital circuit"	USPAT;	2003/12/30
		and switch and pad and (processor or	US-PGPUB	17:53
		microprocessor or cpu) and		
		(microcontroller or "micro controller" or		
	_	micro-controller)	, , , , , , , , , , , , , , , , , , ,	
	1 -		1	

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O- By Author O- Basic O- Advanced Liambo Acadus	1 Coherence multiplexing/subcarrier FDM transmission system with configuration Uehara, H.; Sasase, I.; Communications, Computers, and Signal Processing, 1995. Proceedings. IEEI Pacific Rim Conference on , 17-19 May 1995
O- Join IEEE O- Establish IEEE Web Account	Pages:550 - 553 [Abstract] [PDF Full-Text (308 KB)] IEEE CNF
O- Access the IEEE Member Digital Library	2 A simple scheme for slot reuse without latency for a dual bus configuration Sharon, O.; Segall, A.; Networking, IEEE/ACM Transactions on , Volume: 1 , Issue: 1 , Feb. 1993 Pages:96 - 104
	[Abstract] [PDF Full-Text (836 KB)] IEEE JNL
	3 Global wire bus configuration with minimum delay uncertainty Li-Da Huang; Hung-Ming Chen; Wong, D.F.; Design, Automation and Test in Europe Conference and Exhibition, 2003, 20 Pages: 50 - 55
	[Abstract] [PDF Full-Text (KB)] IEEE CNF
	4 A digital bus configuration for a multi-processor system with squar pyramid structure

Tevfik Akgun, B.; Emre Harmanci, A.;

Electrotechnical Conference, 1994. Proceedings., 7th Mediterranean, 12-14 F 1994

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